

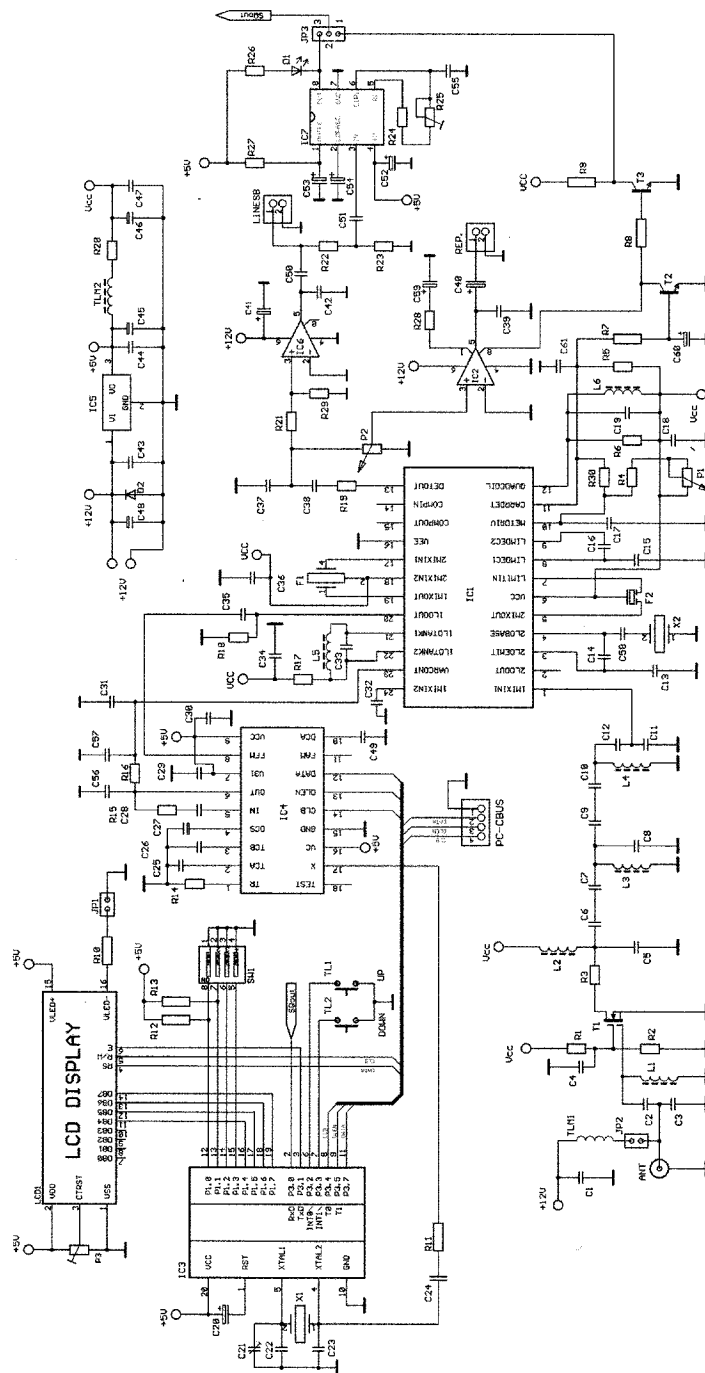


Fig 1 : Circuit diagram of receiver.